



Date: 29-02-2024



Corrigendum -VI

Sub: Tender for “Supply, Design, Installation, Testing, Commissioning, and Integration of OSS solution at Rail-Tel Network (IP-MPLS & DWDM)”.

Ref: (i) This office E-Tender No. RailTel/Tender/OT/CO/NTP/2023-24/OSS/008, Dated 10-11-2023

(ii) Corrigendum-I dated 14.12.23

(iii) Corrigendum-II dated 27.12.23

(iv) Corrigendum-III dated 12.01.24

(v) Corrigendum-IV dated 31.01.24

(vi) Corrigendum-V dated 15.02.24

In reference to the Tender for the “Supply, Design, Installation, Testing, Commissioning, and Integration of OSS solution at Rail-Tel Network (IP-MPLS & DWDM)” against **E-Tender No. RailTel/Tender/OT/CO/NTP/2023-24/OSS/008**, Dated 10-11-2023, following is issued with the approval of competent authority:

1: Corrigendum-VI (point 1-4)

2: Response to firm's queries (point 1- 4)

3: Extension of Bid End Date/Time as mentioned below:

Present	Revised Tender closing/opening date and time
Bid End Date/Time 29-02-2024 15:00:00	Bid End Date/Time 15-03-2024 15:00:00
Bid Opening Date/Time 29-02-2024 15:30:00	Bid Opening Date/Time 15-03-2024 15:30:00

All other terms and conditions will remain unchanged.


29/2/24

(Himanshu Kumar)

GM/Technology Planning

(For and behalf of RailTel Corporation of India Ltd.)

रेलटेल कॉर्पोरेशन ऑफ इंडिया लिमिटेड, भारत सरकार (रेल मंत्रालय) का उपक्रम

CIN : L64202DL2000GOI107905

Registered & Corporate Office : Plate-A, 6th Floor, Office Block, Tower-2, East Kidwai Nagar, New Delhi - 110023
T : +91 11 22900600, F +91 11 22900699 | Website : www.railtelindia.com

Corrigendum-VI for E-Tender No. RailTel/Tender/OT/CO/NTP/2023-24/OSS/008

S.No.	Tender Clause No.	Original Clause	modified clause
1	8.2.7.1 Server -point 2 : Supported CPU	Up to two 3rd Generation Intel® Xeon® Scalable processors with up to 40 cores per processor.	Up to two 3rd Generation Intel®Xeon® / AMD EPYC Scalable processors with up to 40 cores per processor.
2	8.2.7.1 Server -point 3 : Configured CPU	2*32 cores @ 2 GHz, 48 MB Cache (Gold 6338) or better	2*32 cores @ 2 GHz, 48 MB Cache (Gold 6338 / AMD EPYC 7513) or better
3	8.2.7.2 - Storage (point 13)	The proposed array should support mixing of different capacity and type (SSD, SAS, NLSAS) of drives in storage pool. Single storage pool should be accessible to both controllers. Proposed storage should also support growing capacity by single/double/Raid Group drives increment for supporting granular upgrades.	The proposed array should support mixing of different capacity and type (SSD, SAS, NLSAS) of drives in storage pool/Array. Single storage pool should be accessible to both controllers. Proposed storage should also support growing capacity by single/double/Raid Group drives increment for supporting granular upgrades.
4	8.2.7.2 - Storage (point 20)	Proposed storage must have HIPPA compliant, TLS 1.2 support, native SHA2 certificate support, FIPS 140-2 Level 1 certification, EN/IEC certification, KMIP compliant.	Proposed storage must have TLS 1.2 support, native SHA2 certificate support, FIPS 140-2 Level 1 certification, EN/IEC certification, KMIP compliant.
5	New Clause: Chapter-2 SCHEDULE OF REQUIREMENT (SOR) point (t) under note		Bidder/OEM can also leverage existing Network Management system/SDN/EMS deployed in RailTel in proposal. In case if the cards/modules/Licenses in the existing system is proposed, OEM shall provide Undertaking for long term support for 8 years for all existing components irrespective of End of Life of the existing system. Hardware/Software required for such up gradation shall be included in price bid.

Deepak
80.DGM/IT/CO
29.02.24.

Response to Queries related to E-Tender No. RailTel/Tender/DT/CO/NTP/2023-24/OSS/008

S.No.	Tender Clause No.	Original Clause	Proposed clause	Bidder's query	Justification of change	RailTel's Response
1	8.2.7.1 Server -point 2 : Supported CPU	Up to two 3rd Generation Intel® Xeon® Scalable processors with up to 40 cores per processor.	Up to two 3rd Generation Intel® Xeon® Scalable processors with up to 40 cores per processor.			Please see corrigendum-VI
2	8.2.7.1 Server -point 3 : Configured CPU	2*32 cores @ 2 GHz, 48 MB Cache (Gold 6338) or better	2*32 cores @ 2 GHz, 48 MB Cache (Gold 6338 / AMD EPYC 7513) or better			Please see corrigendum-VI
3	8.2.7.2 - Storage (point 13)	The proposed array should support mixing of different capacity and type (SSD, SAS, NLASAS) of drives in storage pool. Single storage pool should be accessible to both controllers. Proposed storage should also support growing capacity by single/double/Raid Group drives increment for supporting granular upgrades.	The proposed array should support mixing of different capacity and type (SSD, SAS, NLASAS) of drives in storage array. Single storage pool should be accessible to both controllers. Proposed storage should also support growing capacity by two drive increment for supporting granular upgrades.		The mixing of drives of different capacity is not recommended at pool level as impacts the adequate utilization, so the mixing of drives should be asked at array level & for proper capacity balancing, minimum two drive upgrade should be asked for.	Please see corrigendum-VI
4	8.2.7.2 - Storage (point 20)	Proposed storage must have HIPPA compliant, TLS 1.2 support, native SHA2 certificate support, FIPS 140-2 Level 1 certification, EN/IEC certification, KMIP compliant.	Proposed storage must have TLS 1.2 support, native SHA2 certificate support, FIPS 140-2 Level 1 certification, EN/IEC certification, KMIP compliant.		HIPPA Compliance is applicable for healthcare industry and as per our understanding is not relevant in this project. We do have other Security certificates relevant to Storage Systems.	Please see corrigendum-VI

Deepak